

Product Code . JL-LA-10450



PC Based Logic Analyzer

Description

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Sophisticated Trigger Captures the Data you want

The continuously variable pre/post trigger allows the trigger to be placed anywhere within the data buffer.

Up to 512 K events, before or after the trigger can be recorded.

The captures the data that is important for your logic analysis.

These powerful capabilities, come in handy for finding the cause of an error condition, and the large buffer to record the sequence of events leading up it.

Sophisticated 16 level triggering allows fine tuning of the exact point to start data captures.

Deep Data Buffers Capture Everything that You Need

In some situations where it is difficult to pinpoint an exact event for triggering, the breadth of a 512 K data buffer can still capture the desired data.

The deep data buffer offers flexibility of storing a great number of events surrounding the trigger.

Features:-

Store up to 512 K events surrounding the trigger point.

Up to 160 data input channels.

External trigger output. Use this to trigger other instruments, like your scope.

Max Sampling Rate 500 MHz.

Simply put, the deeper the buffer the better.

Optional very high speed pattern generator with rates up to 100 Mbits/s from 8 to 160 channels

Continuously variable pre/post trigger position.

High data bandwidth of 100 MHz.

8 external channels high speed clock inputs with user definable combinations for flexible clock qualifying.

Deep data buffers (up to 512 K samples per channel/ 500 MHz).

Captures both timing and state simultaneously with one probe.

High impedance probes minimize interface with the circuit under test(200 Kohm/5 pf).

Variable threshold voltage between -6.4 -+6.4 V.

Work with up to 8 different logic thresholds including TTL, ECL, 3 V logic, COMS, or RS-232 at the same time with multiple threshold voltage selections.
