Product Code . JL-DEECP-1161

Logic Gates Experiment

Description

Logic Gates Experiment

- To verify the truth table for TTL AND, OR, NOT, NAND, NOR. Instrument comprises of 5V Power Supply for logic '1' & logic '0'.
- TTL IC's for 'AND', 'OR', 'NOT' gates & Components soldered on the front panel & connections brought out at sockets.

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