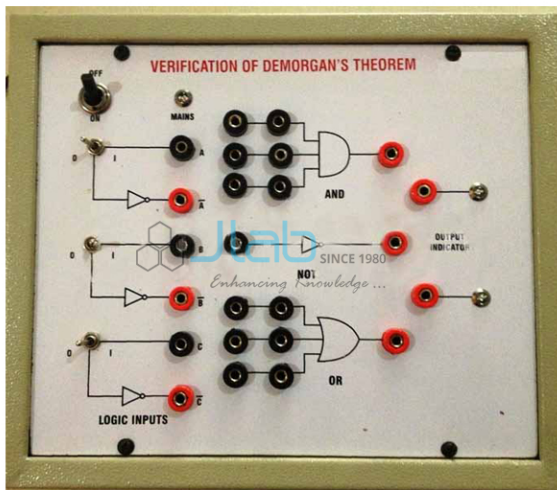


Product Code . JL-DEECP-1162

Verification of Boolean Identities and Demorgan Theorems



Description

Verification of Boolean Identities and Demorgan Theorems

To verify the truth table of 3 input AND gate, NAND gate, NOR gate, single input NOT gates, Boolean expressions & Demorgan Theorems.

Instrument comprises of DC Power Supply, 4 switches for selecting logic '1' & logic '0', 2 Red LED output indicators, circuit diagram printed for 5 'OR', 5 'AND' & 4 'NOT' gates & their connections brought out at sockets.

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