



Product Code . JL-DEECP-1164

Verification of truth tables of Logic gates using NAND Gate

Description

Instrument comprises of Power Supply 2 Red LED output indicators, circuit diagram printed for 4 'NAND', and gates & their respective IC's placed inside the cabinet & connections brought out at sockets.

To verify the truth table of AND gate, OR gate, NAND gate, NOR gate & NOT gate using NAND.

Contact JLab Export for your Educational School Science Lab Equipments. We are best technical educational equipments manufacturers, technical educational equipments manufacturer, technical educational equipments supplier, technical educational instruments, technical educational instruments exporter, technical educational instruments manufacturer, vocational training lab instruments exporter.
