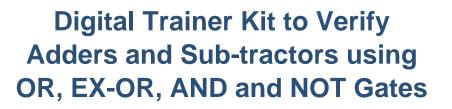


Jain Laboratory Instruments



Product Code . JL-DEECP-1166



Description

To verify the truth table of Digital Half Adder, Half Sub-tractor, Full Adder & Full Sub-tractor using OR, AND, EX-OR & NOT Gates.

Instrument comprises of Power Supply 5V for logic inputs, 4 switches for selecting logic '1' & logic '0', 2 LED output indicators, circuit diagram printed for 1 two input 'OR' Gate, 2 two input 'AND' Gates, 2 'NOT' Gates & 2 EX-OR Gates & their respective IC's placed inside the cabinet & connections brought out at socketsTE F65D Digital Trainer to Verify Full Sub-tractor using OR, EX-OR & AND Gates.

Digital Trainer to Verify Half Adder using OR, EX-OR & AND Gates.

TE F65B Digital Trainer to Verify Half Sub-tractor using OR, EX-OR & AND Gates.

TE-F65C Digital Trainer to

Verify Full Adder using OR, EX-OR & AND Gates.

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