

**Product Code . JL-DEECP-1182**



## Study and Verify Truth tables of RS D Type flip flop using NAND Gates

### Description

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To Verify Truth table of RS D Type flip flops using NAND Gates.

Instrument consist of Power Supply 2 Logic '1' & 2 logic '0' inputs, 1Hz clock pulse, Two output indicators, Circuit diagram for 'RS' flip-flop is Printed & connections for inputs & outputs brought out at the sockets, 1 NOT Gate for converting 'RS' flip-flop to 'D' type flip-flop.

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