

**Product Code . JL-DEECP-1185**

## Study of 2-1 Multiplexer using AND, OR & NOT Gates



### Description

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#### Study of 2-1 Multiplexer using AND, OR & NOT gates

- 2-1 line multiplexer circuit constructed from standard NAND gates acts to control which input ( $I_0$  or  $I_1$ ) Gets passed to the output at Q.
- The 2-input multiplexer connects one of two 1-bit sources to a common output, producing a 2-to-1-line multiplexer.

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