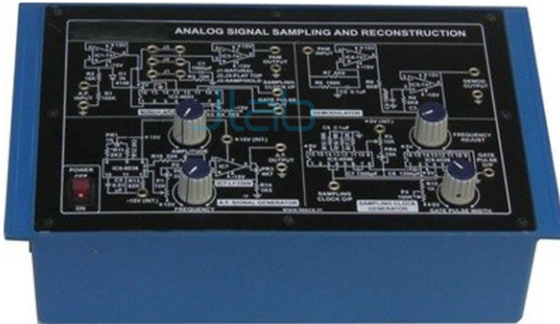


Product Code . JL-CT-1507

Analog Signal Sampling and Reconstruction



Description

Analog Signal Sampling and Reconstruction

Object:

To study Sampling Theorem using Pulse Amplitude Modulation and Demodulation

To generate PAM signal by modulating with audio signal by natural sampling

To generate PAM signal by modulating with audio signal by Flat-Top sampling

To generate PAM signal by modulating with audio signal by Sample / Hold circuit

To demodulate using Low Pass Filter to reconstruct input(sine wave)

Features:

The board consists of following built in parts:

±15V D.C. at 100mA, IC Regulated Power Supply

+5V D.C. at 100mA, IC Regulated Power supply

A.F. signal generator for modulating signal

Sampling clock generator to provide sampling clock and gate pulse

Modulator circuit for Flat-top natural and S/H circuit

Demodulator circuit to reconstruct input (sinewave)

Mains ON/OFF switch, Fuse and Jewel light

Adequate no. of patch cords stackable 4mm spring loaded plug length ½ metre

Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections/observation of waveforms

Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References

Other Apparatus Required:

Dual Scope Cathode Ray Oscilloscope 20MHz

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