

Jain Laboratory Instruments





Product Code . JL-DET-1520

Training Board of Flip Flops

Description

Training Board of Flip Flops

The output of the Flip-Flops can be observed with the help of logic level indicators (LEDs), which are provided on the panel.

The board is absolutely self contained and requires no other apparatus. Practical experience on this board carries great educative value for Science and Engineering Students.

Training Board of Flip Flops Manufacturer India, Training Board of Flip Flops Exporters, Electronics Training Board of Flip Flops, Training Board of Flip Flops Digital Electronics Trainer, Buy Training Board of Flip Flops Online India, Lab Exporters India, Electronics Equipments For School Lab, Didactic, Didactic Transmission Electrical Training Equipment Exporters and Educational Equipments, Electrical Training Equipment Suppliers, Electronics Instruments and Equipments Manufacturer.

Object:

To construct the following Flip-Flops and study their characteristics: R.S. Flip-Flop without clock

R.S. Flip-Flop with clock

D Flip-Flop

J-K Flip-Flop

T Flip-Flop

Master Slave J-K Flip-Flop

Features:

The board consists of the following built-in parts :

+5V D.C. at 100mA, IC regulated power supply

Four, 2-input NAND gates

Four, 3-input NAND gates

One inverter (NOT gate)

Four LEDs with driver circuit to observe the output of flip-flops

A pulser to provide the pulses manually for triggering

Adequate no. of other Electronic Components

Mains ON/OFF switch, Fuse and Jewel light

The unit is operative on 230V ±10% at 50Hz A.C. Mains

Adequate nos. of patch cords stackable from rear both ends 4mm spring loaded plug, length $\frac{1}{2}$ metre

Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms

Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References

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