

Jain Laboratory Instruments







Product Code . JL-EWB-8762

Digital Electronic Trainer

Description

Digital Electronic Trainer

Description:-

Study of basic gates and verification of their truth tables:-

NOT, OR, AND, NOR, NAND, EX-OR, EX-NOR.

Study and verifications of the law of Boolean algebra and De-Morgan's Theorems.

Study of important TTL terminologies. Verification of important TTL Circuit parameters.

Construction and verification of various types of flip-flops using gates and Ic's:

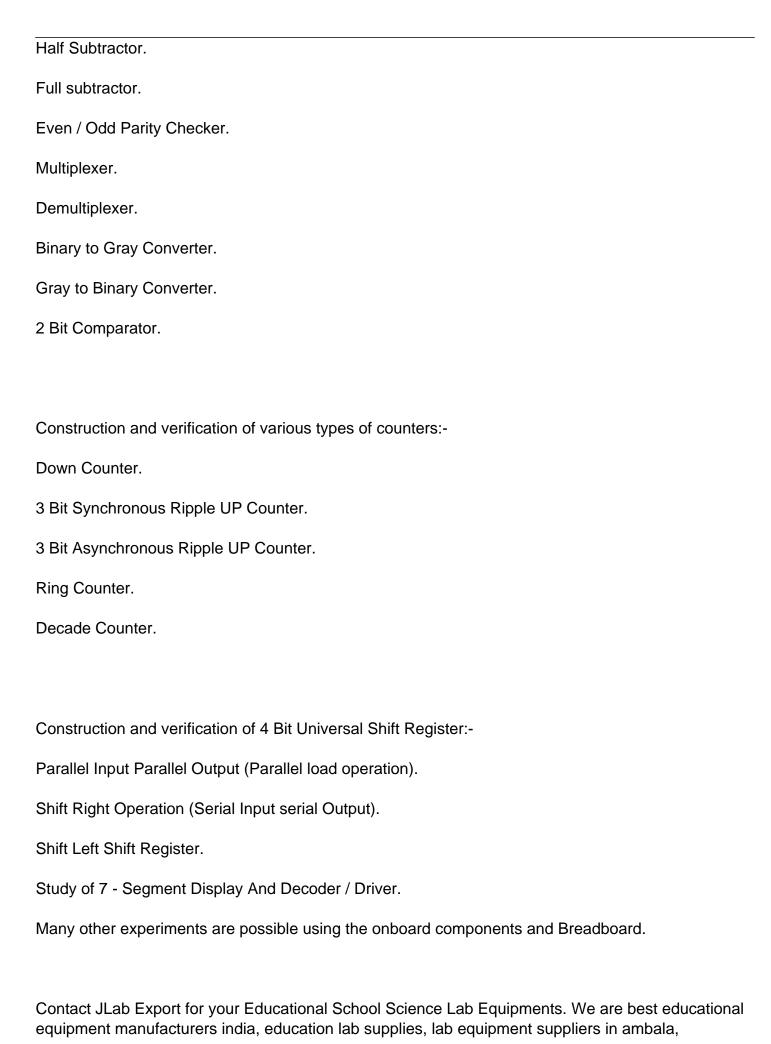
RS Flip-flop 4.2 J-K Flip-flop.

D Flip-flop 4.4 T Flip-flop.

Construction and verification of various types of combinational circuits:-

Half Adder.

Full Adder.



laboratory glassware manufacturers in india, school science lab equipment suppliers in india, science lab equipment for schools, educational lab equipment manufacturer.					